



Dr D Y Patil Pratishthan's
Dr. D.Y. Patil Institute of Engineering, Management and
Research, Akurdi, Pune

DI No.:

ACAD/DI/06

Academic Year:
2021-22

Master Timetable

Revision : 00

Dated : 20/11/2019

Term – I

Department : Electronics & Telecommunication
Engineering

Date of Preparation : 2/8/2021

Day	Div.	10:00 TO 11:00	11:00 TO 12:00	12:00 TO 01:00	01:00- TO 1:40	1:40-2:40	2:40-3:40	3:40-4:40	4:40-5:50
MONDAY	S.E	Elex. Ckt. (RG)	DS (MK)	Elect. Ckt. (PK)	R E A K	Elex. Ckt./Elex. Ckt. /DS S1/S2/S3 PK/RG/MK		DC (MK)	M-III (S1) (tut)
	T.E	DM (PC) (E)	MC (LB)	CN (LG)/ JAVA (F3)		EMT (AN)	DM/MC/CN/Skill Development T1/T2/T3/T4 PC/LB/LG/SRS		EMT(T1)
	B.E.	CN&S/VLSI/RMT/DIV P B1/B2/B3/B4 DNH/PK/AN/SRS		DIVP (SRS) / Embedded & RTOS (LB)		RMT(LG)	AI (AN)	CN&S (DNH)	
TUESDAY	S.E	Elex. Ckt. (RG)	M-III (SJ)	Elect. Ckt. (PK)		ESD/DS/Elect. Ckt. /Elex. Ckt. S1/S2/S3/S4 LG/MK/PK/RG		DC (MK)	M-III (S2) (tut)
	T.E	CN (LG)/ JAVA(F3) (E)	DM (PC)	MC (LB)		EMT (AN)	DC/DM/MC/Skill Development T1/T2/T4/T3 DNH/PC/LB/SRS		EMT(T2)
	B.E.	CN&S/VLSI/RMT/E&R TOS B2/B3/B4/B1 DNH/PK/AN/LB		CN&S (DNH)		DIVP (SRS) / Embedded & RTOS (LB)	AI (AN)	RMT(LG)	
WEDNESDAY	S.E	DS (MK)	M-III (SJ)	Elect. Ckt. (PK)		DC/DC/ESD /ESD S1/S2/S3/S4 SRS/SRS/RG/RG		DC (MK) (E)	M-III (S3) (tut)
	T.E	DC (DNH) (E)	DM (PC)	DC (DNH)		EMT (AN)	DC/DM/MC/CN T4/T3/T1/T2 DNH/PC/LB/LG		EMT(T3)

WED	B.E.	CN&S/VLSI/RMT/E&R TOS B3/B4/B1/B2 LG/PK/AN/LB		RMT(LG)	B	CN&S (DNH)	AI (AN)	VLSI (PK)		
	THURSDAY	S.E	DC (MK)	M-III (SJ)		Elect. Ckt. (E) (PK)	G	DS/Elect. Ckt/Elect. Ckt/DS S1/S2/S3/S4 MK/RJ/RJ/MK		Elex. Ckt. (RG)
T.E		MC (LB) (E)	DM (PC)	DC (DNH)	N	MC (LB)		DC/DM/CN/Skill Development T3/T4/T1/T2 DNH/PC/LG/SRS		EMT(T4)
B.E.		CN&S/VLSI/RMT/DIV P B4/B1/B2/B3 LG/PK/AN/SRS		DIVP (SRS) / Embedded & RTOS (LB)		O		CN&S (DNH)	VLSI (PK)	VLSI (PK)
FRIDAY	S.E	M-III (SJ)	M-III (E) (SJ)	Elex. Ckt. (RG) (E)			L	ESD/DC/Elect. Ckt. S2/S3/S4 LG/MK/RG		DS (MK) (E)
	T.E	EMT (AN) (E)	CN (LG)/ JAVA(F3)	CN (LG)/ JAVA (F3)	J			DC (DNH)	DC/JAVA/MC/Skill Development T2/T4/T3/T1 DNH/F3/LB/SRS	
	B.E.	RMT(LG) (E)	CN&S (DNH) (E)	DIVP (SRS) / Embedded & RTOS (LB) (E)		AI (AN) (E)		VLSI (PK) (E)		
SATURDAY	S.E	EVS				B	Elex. Ckt./Elex. Ckt. /DC S1/S3/S4 RG/RG/MK			
	T.E	Saturday activity			Saturday Activity					
	B.E.	Project Phase-I			Project Phase-I					

ABBRV.	NAME OF THE FACULTY
PC	Dr. Priya Charles
LB	Mrs. B. Lakshmiprabha

ABBRV.	NAME OF THE FACULTY
MK	Mrs. Munmun Kakkar
PK	Mr. Pradnyawant Kalmakar

CLASS	ABBRV.	NAME CLASS TEACHER
SE	MK	Mrs. Munmun Kakkar
TE	DNH	Mrs. D. N. Hire

LG	Mr. Lokesh Giripunje
AN	Mrs. Amruta Nikam
DNH	Mrs. D. N. Hire

RG	Mrs. Raji George
SRS	Mrs. Sandhya Shinde

BE	LG	Mr. Lokesh Giripunje
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Time Table In-charge
Mrs. D. N. Hire

Department Academic Coordinator
Mrs. B. Lakshmiprabha

Head of Department
Dr. Priya Charles